

Product Discontinuation Notice: VO48 Pin Package in Platform Flash PROM Devices

XCN09030 (v1.1) December 28, 2009

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing VO48 pin package in certain Platform Flash PROM devices due to supplier discontinuance. This notification applies to Commercial (C) and Military (M-grade) temperature grade products. This updated version 1.1 contains revised dates in the Key Dates and Ordering Information section of this notice and overrides the previously released version 1.0 of this notice published on November 16, 2009.

Description

Due to assembly supplier package line discontinuance, Xilinx is discontinuing the VO48 pin package in all devices of Platform Flash PROM family in Commercial (C) and Military (M-grade) temperature grade products. The customers are advised to download the latest version of this notice from the Xilinx web site at http://www.xilinx.com/support/documentation/customer notices.htm.

Due to more aggressive supplier line discontinuance, Xilinx issued this notification with earlier Final order date, but extended the Final delivery date to assist customers. This notification overrides the previously released version 1.0 of this notice published on November 16, 2009.

Products Affected

The products affected include all standard part numbers and specification control document (SCD) versions of these standard part numbers for VO48 pin package in Platform Flash PROM devices listed in this notice. The affected part number can be replaced with the corresponding VOG48 ('G' for Pb-free) package part as VOG48 package is solder reflow backward compatible with the VO48 pin package. Further more, the external lead plating on the VOG48 will be changed from Matte Tin to Nickel Palladium Gold plating as per Xilinx Product Change Notice XCN09026 planned to be published in the Q2 of year 2010. Affected part numbers and recommended replacement parts are included in Table 1.

Table 1: Platform Flash PROM Products Affected

Commercial Grade Parts		
Part Number	Replacement Part ¹	
XCF08PVO48C	XCF08PVOG48C	
XCF08PVO48C0973	XCF08PVOG48C	
XCF16PVO48C	XCF16PVOG48C	
XCF16PVO48C0973	XCF16PVOG48C	
XCF32PVO48C	XCF32PVOG48C	
XCF32PVO48C0973	XCF32PVOG48C	
Military Grade Parts		
Part Number	Replacement Part ²	
XQF32PVO48M	XQF32PVOG48M (Planned)	

NOTES

- 1. Replacement parts are currently available in Matte Tin plating. The plating in the replacement parts will change from Matte Tin to Nickel Palladium Gold plating as per Xilinx Product Change Notice XCN09026. This notice is planned for release in Q2 of year 2010, with implementation in Q3 of year 2010.
- 2. This part is not available in Matte Tin plating. The part is planned to be available only in Nickel Palladium Gold plating upon qualification completion, targeted for Q2 of year 2010.

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Key Dates and Ordering Information

Final orders are accepted until February 15, 2010.

Final deliveries must occur on or before February 4, 2011.

Return Material Authorizations (RMA) will be accepted until August 5, 2011.

Response

Customers are requested to place last-time-buy orders in a timely manner prior to the key dates listed above to avoid product availability issues. If you anticipate that your last time buy requirements will exceed the current rate, please provide your Xilinx Sales Representative with a forecast of the requirements. All orders will be supported on a first-come, first-served basis with the non-cancellable and non-returnable (NCNR) condition. For additional information or questions, contact your local Xilinx Sales representative.

Important Notice: Xilinx Customer Notifications (XCNs, XDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (http://www.xilinx.com/support). Register today and personalize your "MyAlerts" area to include Customer Notifications. Xilinx MySupport provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to Xilinx Answer Record 18683.

Revision History

The following table shows the revision history for this document:

Date	Version	Description of Revisions	
11/16/09	1.0	Initial release.	
12/28/09	1.1	Revised all dates in the Key Dates and Ordering Information section	

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